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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Gerard Chauvel, et al.

Serial No.: 10/631,246

Filed: July 31, 2003

For: Processor That Accommodates Multiple Instruction Sets and Multiple Decode Modes

TI-35434

Art Unit: 2183

Examiner:

Conf. No: 1610

RECEIVED

MAR 30 2004

Technology Center 2100

LETTER TO THE OFFICIAL DRAFTSPERSON

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)
I hereby certify that the above correspondence is being
deposited with the U.S. Postal Service as First Class Mail in an
envelope addressed to: Commissioner for Patents, P.O. Box
1450, Alexandria, VA 22313-1450, on January 29, 2004.


Robin E. Barnum

Sir:

Please find enclosed the formal drawings for the subject case. Charge any necessary fees to Texas Instruments Incorporated, Deposit Account No. 20-0668. This form is submitted in triplicate.

Respectfully submitted,



Robert D. Marshall, Jr.
Attorney for Applicant(s)
Reg. No. 28,527

Texas Instruments Incorporated
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